



*COJ*  
PATENT  
HIT02 P-113

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Imad Mahawili  
Serial No. : 10/074,287  
Filing Date : February 12, 2002  
Patent No. : 6,965,092  
Issue Date : November 15, 2005  
Entitled : ULTRA FAST RAPID THERMAL PROCESSING CHAMBER AND  
METHOD OF USE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

Dear Sir:

I hereby certify that the accompanying return postcard, Request for Certificate of Correction (1 page), and Form PTO-1050 (1 page, in duplicate) are being deposited in the United States Postal Service as First Class Mail, in an envelope addressed to:

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

on October 25, 2006.

Catherine S. Collins  
Registration No. 37 599  
2851 Charlevoix Drive, S.E. Suite 207  
P.O. Box 888695  
Grand Rapids, MI 49588-8695  
(616) 975-5506

*Certificate  
NOV 01 2006  
of Correction*

CSC:clw  
Enclosures

*(NOV - 2 2006)*



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HIT02 P-113

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Applicant(s) : Imad Mahawili  
Serial No. : 10/074,287  
Filing Date : February 12, 2002  
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METHOD OF USE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION

Dear Sir:

A request is being made for Certificate of Correction in the above-identified patent, which issued with the following errors identified by Column and Line from the patent.

Column 4:

Line 40, "semicondeuctor" should be --semiconductor--.  
Line 43, "wafers" should be --wafer,--.

Column 6:

Line 60, "speeds" should be --speed,--.

Column 12:

Line 16, Claim 1, "end" should be --and--.

Enclosed is the Certificate of Correction Form PTO-1050 (in duplicate) identifying errors by Column and Line from the patent which are chargeable to the Official Printer.

Respectfully submitted,

IMAD MAHAWILI

By: Van Dyke, Gardner, Linn & Burkhart, LLP

Date: October 25, 2006

Catherine S. Collins  
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# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

PATENT NO. : 6,965,092 B2  
APPLICATION NO. : 10/074,287  
DATED : November 15, 2005  
INVENTOR(S) : Imad Mahawili

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4:

Line 40, "semicondektor" should be --semiconductor--.  
Line 43, "wafers" should be --wafer,--.

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---

MAILING ADDRESS OF SENDER:

Catherine S. Collins  
Van Dyke, Gardner, Linn  
& Burkhart, LLP  
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Grand Rapids, MI 49588-8695

PATENT NO. 6,965,092 B2

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